HE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Norio Hirashita et al.

Serial No.: 09/825,973

Filed: April 5, 2001

Attn: Applications Branch

Attorney Docket No.: OKI.227

For: A STRUCTURE OF A FIELD EFFECT TRANSISTOR HAVING METALLIC

SILICIDE AND MANUFACTURING METHOD THEREOF

CLAIM OF PRIORITY

Honorable Assistant Commissioner for Patents and Trademarks, Washington, D.C. 20231

Sir:

Date: September 27, 2002

Applicants, in the above-identified application, hereby claim the priority date under the International Convention of the following Japanese application:

Appln. No. 2000-104733

filed April 6, 2000

as acknowledged in the Declaration of the subject application.

A certified copy of said application is being submitted herewith.

Respectfully submitted,

VOLENTINE FRANCOS, P.L.L.C.

Andrew J. Telesz, Jr. Registration No. 33,581

12200 Sunrise Valley Drive, Suite 150 Reston, Virginia 20191 Tel. (703) 715-0870

Fax. (703) 715-0877

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日本国特許庁 JAPAN PATENT OFFICE

別紙添付の書類に記載されている事項は下記の出願書類に記載されている事項と同一であることを証明する。

This is to certify that the annexed is a true copy of the following application as filed with this Office

出願年月日 Date of Application:

2000年 4月 6日

出願番号 Application Number:

特願2000-104733

上 原 人 pplicant(s):

沖電気工業株式会社

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2001年 4月20日

特許庁長官 Commissioner, Japan Patent Office





ATTY DOC T#: OKI.227

DUE DA : September 28, 2002

APPLICANT: Norio Hirashita et al.

SERIAL NO.: 09/825,973

FILING DATE: April 5, 2001

SEP 2 7 2002

TITLE:

A STRUCTURE OF A FIELD EFFECT TRANSISTOR HAVING

METALLIC SILICIDE AND MANUFACTURING METHOD

THEREOF

RECEIPT OF THE FOLLOWING PAPERS IS ACKNOWLEDGED:

Amendment Transmittal Letter; Response to Restriction Requirement and Amendment; Version with marked-up changes and Claim of Priority including 1 document.

DATE: September 27, 2002

ÝTLA :YTT*P*

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řeck No. <u>n/a</u>]